
GIRISH MALLYA UDUPI

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Objective:

Seek a challenging research position in the field of Signal Processing.

Education:

M.S., Electrical Engineering

Illinois Institute of Technology (IIT), Chicago, IL

Present (Current GPA 4.0/4.0)

Bachelor of Engineering (B.E.), Electronics and Communication

SDM College of Engineering and Technology (SDMCET), India

2007 (Aggregate 68.73%)

Academic Project:

As part of the curriculum in my B.E., 8th semester, I, along with three others took up the project "Transistor-level simulation of a 16-bit Multiply-Accumulate (MAC) unit".

The functionality of the MAC unit was first tested at the block level using VHDL and then, using SPICE, every transistor of the unit was described and the unit simulated.

Experience:

Programmer Analyst Trainee, Cognizant Technology Solutions India Pvt Ltd (November 2007 to July 2008)

Software Skills:

Windows 98/XP/Vista, MS Office

C, SQL, Intel 8085/8086 programming languages, Intel 8051 programming language

SSRS, MATLAB, VHDL, Adobe Photodeluxe

Coursework Completed:

Signals and Systems, Digital Signal Processing, DSP Architecture, Communication Systems.

Courses Currently Studying:

Analysis of Random Signals, Statistical Signal Processing, Computer Vision and Image Processing.